

[54] DATA TERMINAL

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[56] References Cited

U.S. PATENT DOCUMENTS

2,790,899	4/1957	Townsend	455/351 X
3,230,533	6/1966	Brill	455/346 X
3,518,681	6/1970	Kiepe	455/292 X
3,720,874	3/1973	Gorcik et al.	343/841 X
3,816,836	6/1974	Smith	343/841 X

3,976,995	8/1976	Sebestyen	455/351 X
4,286,335	8/1981	Eichler et al.	455/346 X

FOREIGN PATENT DOCUMENTS

0023289 2/1981 European Pat. Off. 455/346

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[57]

ABSTRACT

A data terminal usable with a paging receiver mounted thereon is disclosed. The data terminal includes a whip antenna and a resonance circuit which is connected to the whip antenna. The ground of the resonance circuit is connected to the ground of circuitry mounted in the data terminal, so that the ground of the latter serves as a reflector of the whip antenna to enhance the antenna gain as well as the receive sensitivity. The antenna of the receiver is sufficiently remote from a clock oscillating section for a CPU which is installed in the data terminal, whereby clock noise generated by the clock oscillating section is prevented from deteriorating the receive sensitivity.

4 Claims, 2 Drawing Sheets

